

CURRICULUM VITAE

Dr. MANJUNATHA REDDY H S

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Global Academy of Technology
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Date of Birth: 23/06/1968

Sex: Male

Father's Name: SRIRAMA REDDY

1. Qualification:

Sl. No.	Degree	University	Class
1	Ph.D., (Electronics and Communication Engg)	Jawaharlal Nehru Technological University, Anantapur (AP)	-----
2	M. Tech., (Digital Electronics)	Visvesvaraya Technological University, Belgaum, Karnataka	FCD
3	B.E., (Electronics)	Bangalore University, Bangalore	SC

2. Work Experience: 23 Years of Teaching & Research Experience

Sl. No.	Place of Work	Designation	Period
1	Global Academy of Technology, Rajarajeshwarinagar, Bangalore	Professor and Head Dept.of E&C Engg.	23/2/2012 – Till date
2	Global Academy of Technology, Rajarajeshwarinagar Bangalore	Professor, Dept.of E&C Engg.	1/8/2008 to 23/2/2012
3	Global Academy of Technology, Rajarajeshwarinagar Bangalore	Assistant professor, Dept.of E&C Engg.	28/8/2007 to 31/7/2008
4	Ghousia College of Engg, Ramanagara, Karnataka	Assistant professor, Dept.of E&C Engg	5/10/2006 to 27/08/2007
	Global Academy of Technology, Rajarajeshwarinagar Bangalore	Assistant professor, Dept.of E&C Engg.	1/9/2004 to 4/10/2006
5	Global Academy of Technology, Rajarajeshwarinagar Bangalore	Lecturer Dept.of E&C Engg.	10/07/2002 to 30/8/2004
6	Ramakrishna polytechnic, Srinivasapur	HOD & i/c Principal	25/5/1993 to 14/10/2001
7	Sir M V Polytechnic, Siddlaghatta	Lecturer Dept.of E&C Engg	1992-1993

3. RESEARCH INTERESTS:

Image Processing, VLSI design, Biometrics, Digital Communication, Digital Signal Processing, Information Security

4. LIST OF PUBLICATIONS

A. International Journals:

- [1] H S Manjunatha Reddy and K B Raja, "High Capacity and Security Steganography using DWT," *International Journal of Computer Science and Security*, vol (3), issue (6), pp.462-472, 2010.
- [2] H S Manjunatha Reddy and K B Raja, "Wavelet based Non LSB Steganography," *International Journal of Advanced Networking and Applications (IJANA)*, vol.3, issue.3, pp.1203-1209, 2011.
- [3] H S Manjunatha Reddy and K B Raja, "Wavelet based Secure Steganography with Scrambled Payload," *International Journal of Innovative Technology and Exploring Engineering*, vol.1, issue.2, pp.121-129, 2012.
- [4] H S Manjunatha Reddy and K B Raja, "Steganography based on Adaptive Embedding of Encrypted payload in Wavelet domain", *International Journal of Scientific and Engineering Research*, Volume 3, Issue 8, pages. 1-8, August-2012.
- [5] H S Manjunatha Reddy and K B Raja, "Hybrid domain based Steganography using BPS, LSB and IWT", *International Journal of Computer Applications*, vol. 54, no.3, pages. 9-16, September 2012.

B. International Conferences:

- [6] H S Manjunatha Reddy, K B Raja, K Suresh Babu, Venugopal K R and L M Patnaik, "Multilevel Wavelet Fusion Steganography," *International conference on Information science, Technology and Management*, Delhi, pp. 42(1-14), July 13-15, 2009.
- [7] H S Manjunatha Reddy, kasim T, K B Raja, "Performance Analysis of DWT and IWT on Non LSB Steganography," *ACEEE Int. Colloquiums on*

Computer Electronics Electrical Mechanical and Civil, pp.42-44, Sep 20-21, 2011.

- [8] H S Manjunatha Reddy, N Sathisha, Annu Kumari and K B Raja, "Secure Steganography using Hybrid Domain Technique," *IEEE Third International Conference on Computing, Communication and Networking Technologies ICCCNT 2012*, pp.1-11. July 2012, Coimbatore
- [9] H S Manjunatha Reddy, N Sathisha, S Deepa and K B Raja, "LWT based Encrypted Payload Steganography", Fourth International Conference on Advances in Recent Technologies in Communication and Computing (ARTCom 2012), Oct-19-20, 2012, Bangalore, India
- [10] H S Manjunatha Reddy, Vijalakshmi Patil and K B Raja, "Adaptive Steganography based on Spatial and Transform domain Techniques", International conference on Electrical and Electronics Engineering, pp. 28-34, 2012.

5. Best Paper Award

- 1. H S Manjunatha Reddy, Vijalakshmi Patil and K B Raja, "Adaptive Steganography based on Spatial and Transform domain Techniques", International conference on Electrical and Electronics Engineering, pp. 28-34, 2012.

6. Course Conducted:

- 1. AICTE-ISTE Sponsored Short Term Training Programme on "Principles of Effective Teaching for Engineering College Teachers," organized by Global Academy of Technology, Bangalore, 4th Aug– 12th August 2004, (Duration: 2 Weeks).
- 2. One day workshop on "Micro strip Antenna", by Global Academy of Technology, Bangalore, 9th April 2009

7. Conferences Attended:

- 1. H S Manjunatha Reddy, K B Raja, K Suresh Babu, Venugopal K R and L M Patnaik, "Multilevel Wavelet Fusion Steganography," *International*

conference on Information science, Technology and Management, Delhi, pp. 42(1-14), July 13-15, 2009

2. H S Manjunatha Reddy, kasim T, K B Raja, "Performance Analysis of DWT and IWT on Non LSB Steganography," *ACEEE Int. Colloquiums on Computer Electronics Electrical Mechanical and Civil*, pp.42-44, Sep 20-21, 2011
3. H S Manjunatha Reddy, N Sathisha, Annu Kumari and K B Raja, "Secure Steganography using Hybrid Domain Technique," *IEEE Third International Conference on Computing, Communication and Networking Technologies ICCCNT 2012*, pp.1-11. July 2012, Coimbatore
4. H S Manjunatha Reddy, N Sathisha, S Deepa and K B Raja, "LWT based Encrypted Payload Steganography", Fourth International Conference on Advances in Recent Technologies in Communication and Computing (ARTCom 2012), Oct-19-20, 2012, Bangalore, India
5. H S Manjunatha Reddy, Vijalakshmi Patil and K B Raja, "Adaptive Steganography based on Spatial and Transform domain Techniques", International conference on Electrical and Electronics Engineering, pp. 28-34, 2012.

8. Certificates/Training/Workshops:

1. Workshop on "Wireless Networks" held at Global Academy of Technology, Bangalore organized by BITES - Wipro on 18th April 2009
2. Workshop on "Issues in Electromagnetic Interference and Electromagnetic Compatibility" under TEQIP held at UVCE, Bangalore on 25th and 26th March 2009.
3. A short term course on System C and Application on VLSI Design held from 6/4/2005 to 8/4/2005 at JSSATE, Bangalore.
4. Workshop on "Digital Signal Processing and its Applications" held at AIT, Chickmagalur, and 14th -15th May 2004.
5. 7th ISTE Karnataka state convention and AICTE sponsored Two day national seminar on Building National Competitive Advantage through Technical Education on 29th and 30th Nov2004 at Dr AIT, Bangalore.

6. AICTE Sponsored Short Term Training Programme on “Digital Signal Processing and Applications”, by Dept. of Electronics and Computer Science, University Visvesvaraya College of Engineering, Bangalore University. 29th July – 14th August 2002, (Duration: 2 Weeks).

9. Subjects Taught:

Digital Signal Processing, Digital image processing, Signal and systems, Control systems, Information Theory and Coding, VLSI Design Fundamental of CMOS VLSI, Analog and mixed mode VLSI design, Digital communication, Network Analysis, Electronic Circuits, Linear Integrated Circuits and Applications, Power Electronics, Networks and Lines and Basic Electronics

10. Professional Activities:

- Coordinator, ISTE Chapter, GAT, Bangalore
- Life Member [LM 41902], Indian Society for Technical Education, New delhi.
- FELLOW member [F-237368],IETE/FIETE
- Organizing Member, Workshop on Advances in VLSI design and Embedded system, organized by GAT.
- Organizing Member, National Conference on Thrust Areas in Engineering (NCTAE-2012) organized by GAT.
- Session Chairperson for the Fourth International Conference on Advances in Recent Technologies in Communication and Computing (ARTCom 2012), Oct-19-20, 2012, Bangalore, India.
- Session Chairperson for the National Conference on Recent Trends in Electronics and Communication Engineering, 31st May 2014, SJBIT, Bangalore, India
- Subject Expert for Interview panel ,SEA College of Engineering, Bengaluru and Don Bosco institute of Technology, Bengaluru for the year 2014

11. Other Activities:

- **Worked as a coordinator, VTU** Valuation center, JSSATE, Bangalore for June/July 2009 Examination.
- **Worked as a Deputy Chief Coordinator, VTU** Valuation center, Global Academy of Technology, Bangalore for Dec2009/Jan2010, June/July 2010 and Dec2010/Jan 2011 Examinations.

- **Worked as a Deputy Chief Coordinator**, VTU digitization center, VTU Regional office, Bangalore for Nov/Dec 2011 Examinations.
- Worked as a Deputy Chief Coordinator, VTU digitization centre, VTU Regional office, Bangalore for June/July 2012 Examinations.
- **BOE member for VTU EC/TC** composite board for the year 2011-2012, 2013-2014 and 2015-2016.
- **VTU LIC committee** member for the year 2013-2014 and 2015-2016
- **VTU EDUSAT** coordinator